



SE05L3BDDAH

ESD PROTECTION DIODE

FEATURES

- Low clamping voltage
- Low leakage current
- Bi-directional Configurations
- Suffix " H " indicated Halogen-free part, ex.SE05L3BDDAH

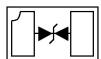
APPLICATIONS

- Laptop Computers
- Notebooks, Desktops, and Servers
- Digital Cameras

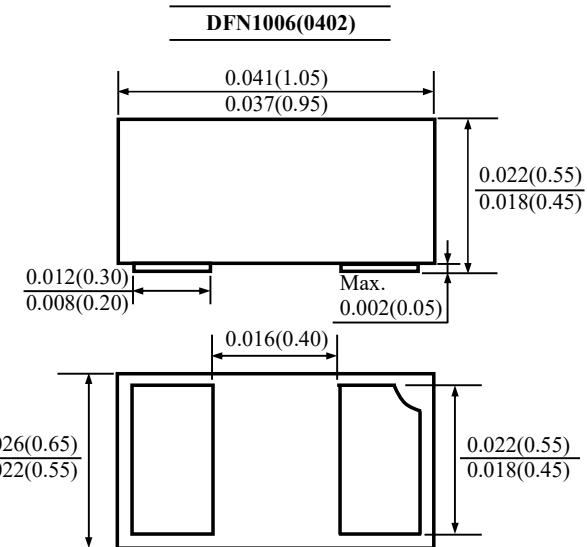
MECHANICAL DATA

Case : DFN1006(0402) mold package

PIN CONFIGURATION



Top View



Dimensions in inch and (millimeter)

Maximum Ratings (Rating at 25°C ambient temperature unless otherwise specified)

Parameter	Symbol	Value	Units
Peak Pulse Power ($t_p=8/20\mu s$)	P_{PP}	55	W
Peak Pulse Current ($t_p=8/20\mu s$)	I_{PP}	4	A
ESD per IEC 61000-4-2 (Air)	V_{ESD}	± 20	kV
ESD per IEC 61000-4-2 (Contact)		± 17	
Operating Junction Temperature Range	T_J	-55 to +125	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C



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Electrical Characteristics (Rating at 25°C ambient temperature unless otherwise specified)

Parameter	Test Condition	Symbol	Min	Typ.	Max	Units
Reverse Stand-Off Voltage	-	V _{RWM}	-	-	5	V
Reverse Breakdown Voltage	I _R =1mA	V _(BR)	7	-	10	V
Reverse Leakage Current	V _{RWM} =5V	I _R	-	-	50	nA
Clamping Voltage	I _{pp} =1A, t _p =8/20μs I _{pp} =4A, t _p =8/20μs	V _C	- -	- -	10 13	V
ESD Clamping Voltage	I _{PP} =4A, t _p =0.2/100ns (TLP) I _{PP} =16A, t _p =0.2/100ns (TLP)	V _C	- -	10.0 13.3	- -	V
Dynamic Resistance (Note 1)	-	R _{DYN}	-	0.28	-	Ω
Junction Capacitance	V _R =0V, f=1MHz	C _J	-	-	0.35	pF

Note :

1. Dynamic Resistance calculated from I_{TLP}=4A to I_{TLP}=16A.

RATINGS AND CHARACTERISTIC CURVES

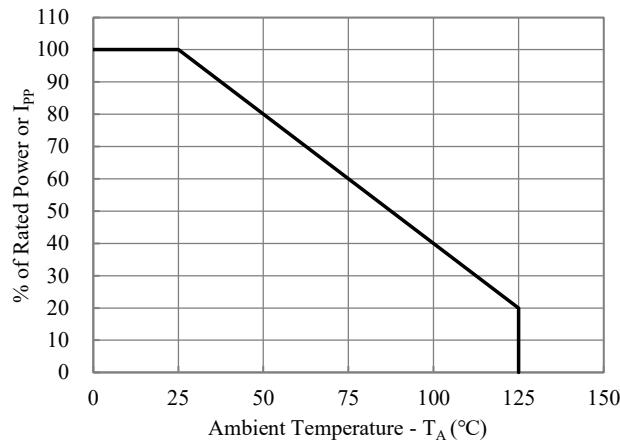


Fig. 1 Power Derating Curve

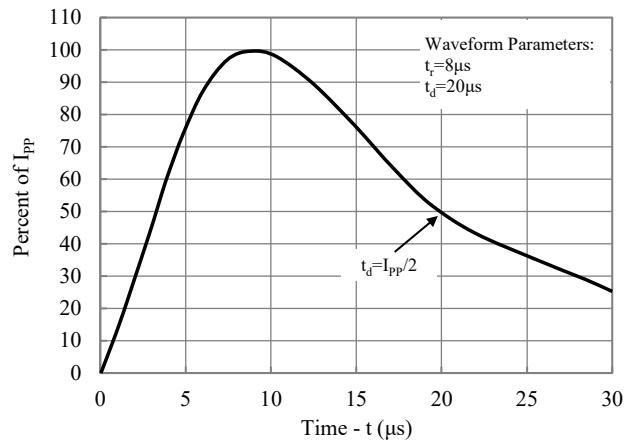


Fig. 2 Pulse Waveform

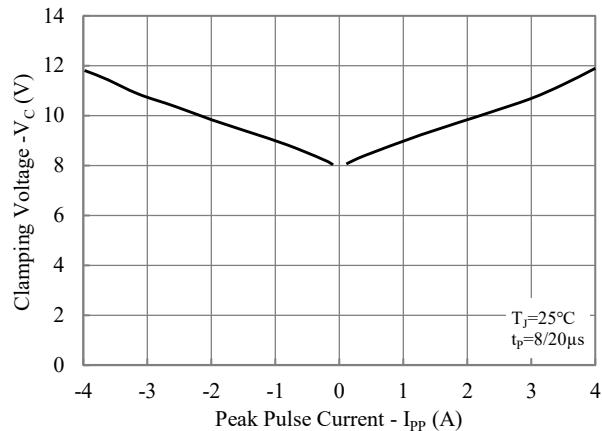


Fig. 3 Clamping Voltage vs. Peak Pulse Current

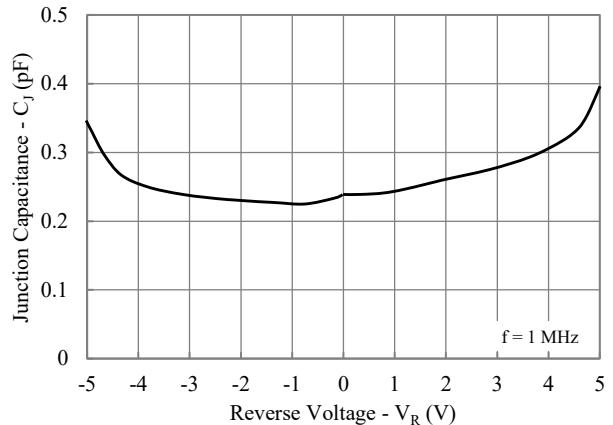


Fig. 4 Junction Capacitance vs. Reverse Voltage

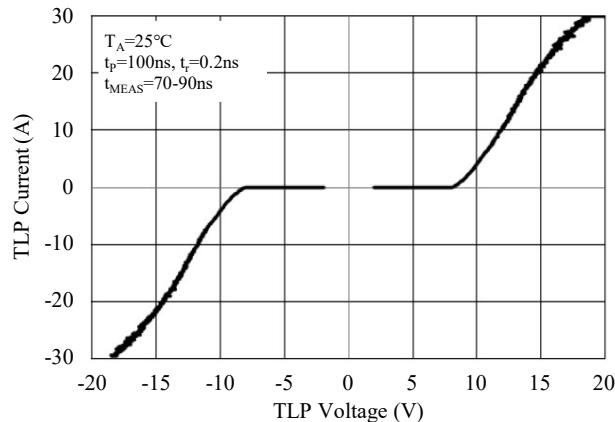


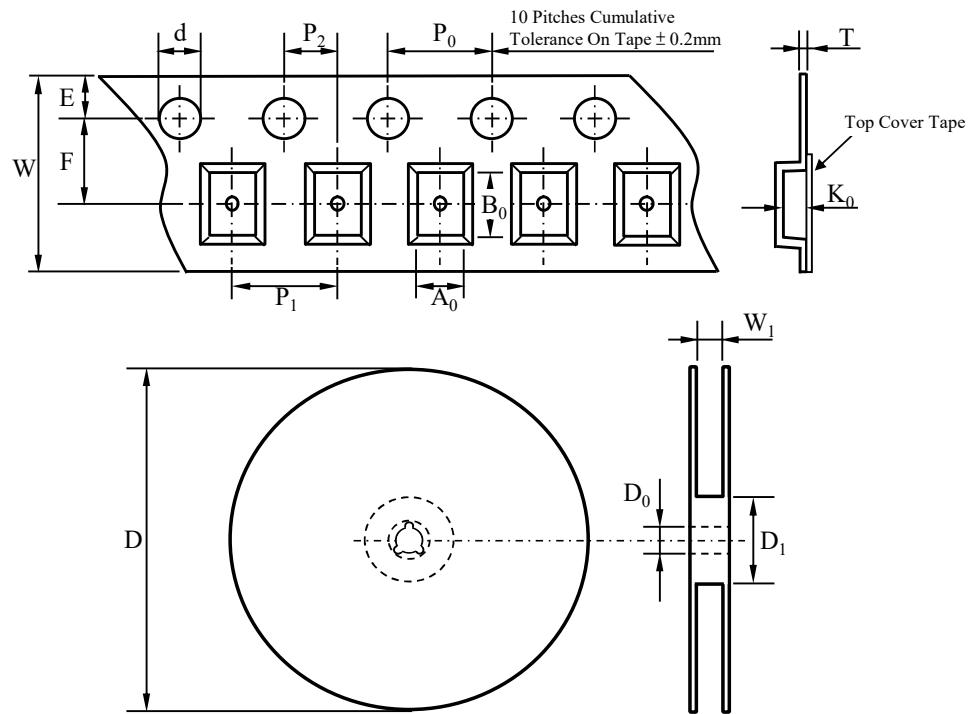
Fig. 5 TLP Characteristic



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TAPE & REEL SPECIFICATION



Item	Symbol	DF1006 (0402)
Carrier width	A ₀	*
Carrier length	B ₀	
Carrier depth	K ₀	
Sprocket hole	d	1.50 ± 0.10
Reel outside diameter	D	178.00 ± 2.00
Feed hole width	D ₀	13.00 ± 0.50
Reel inner diameter	D ₁	MIN. 54.00
Sprocket hole position	E	1.75 ± 0.10
Punch hole position	F	3.50 ± 0.10
Sprocket hole pitch	P ₀	4.00 ± 0.10
Punch hole pitch	P ₁	2.00 ± 0.10
Embossment center	P ₂	2.00 ± 0.10
Overall tape thickness	T	MAX. 0.60
Tape width	W	8.00 ± 0.30
Reel width	W ₁	8.40 ± 1.50

Note *: A₀, B₀, and K₀ are determined by component size. The clearance between the components and the cavity must be within 0.05 mm min. to 0.5 mm max.

ORDER INFORMATION

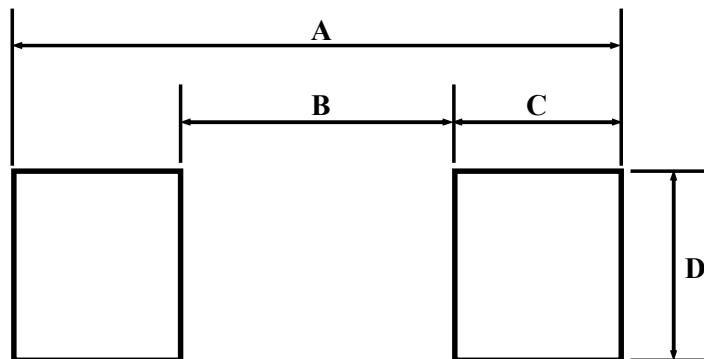
Package	Marking Code	Reel Size	Quantity
SE05L3BDDAH	ER	7"	10,000



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SUGGESTED SOLDER PAD LAYOUT



Unit :mm

PACKAGE	A	B	C	D
DFN1006	1.10	0.30	0.40	0.60